

wherein a gray scale display level of one frame period corresponds to a value obtained by averaging gray scale voltage levels inputted in each subframe period contained in said one frame period.

2.(Amended) A display device comprising:

an active matrix circuit comprising a plurality of pixel TFTs disposed in matrix; and

a source driver and a gate driver which drive said active matrix circuit,

wherein n bit information out of m bit digital video data inputted from an external is used for a voltage gray scale method, and (m-n) bit information is used for a time ratio gray scale method, where said m and said n are integers equal to or larger than 2 and satisfy $m > n$,

wherein a gray scale display level of one frame period corresponds to a value obtained by averaging gray scale voltage levels inputted in each subframe period contained in said one frame period, and

wherein said one frame period comprises 2^{m-n} subframe periods.

3.(Amended) A display device comprising:

an active matrix circuit comprising a plurality of pixel TFTs disposed in matrix over a substrate;

a source driver and a gate driver which drive said active matrix circuit over said substrate;

and

a circuit which converts m bit digital video data inputted from an external into n bit digital video data and provides said n bit digital video data to said source driver, where said m and said n

are integers equal to or larger than 2 and satisfy $m > n$, wherein said circuit is formed over said substrate, and

wherein one frame image comprises 2^{m-n} subframes.

4.(Amended) A display device comprising:

an active matrix circuit comprising a plurality of pixel TFTs disposed in matrix over a substrate;

a source driver and a gate driver which drive said active matrix circuit over said substrate; and

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a circuit which converts m bit digital video data inputted from an external into n bit digital video data and provides said n bit digital video data to said source driver, where said m and said n are integers equal to or larger than 2 and satisfy $m > n$, wherein said circuit is formed over said substrate, and

wherein one frame image comprises 2^{m-n} subframes.

5.(Amended) A display device comprising:

an active matrix circuit comprising a plurality of pixel TFTs disposed in matrix; and

a source driver and a gate driver which drive said active matrix circuit,

wherein n bit information out of m bit digital video data inputted from an external is used for a voltage gray scale method and (m-n) bit information is used for a time ratio gray scale method, where said m and said n are integers equal to or larger than 2 and satisfy $m > n$,

wherein a gray scale display level of one frame period corresponds to a value obtained by averaging gray scale voltage levels inputted in each subframe period contained in said one frame period, and

wherein an image is displayed by an image gray scale of $(2^m - (2^{m-n} - 1))$ patterns.

6.(Amended) A display device comprising:

an active matrix circuit comprising a plurality of pixel TFTs disposed in matrix; and

a source driver and a gate driver which drive said active matrix circuit,

wherein n bit information out of m bit digital video data inputted from an external is used for a voltage gray scale method and (m-n) bit information is used for a time ratio gray scale method, where said m and said n are integers equal to or larger than 2 and satisfy $m > n$,

wherein a gray scale display level of one frame period corresponds to a value obtained by averaging gray scale voltage levels inputted in each subframe period contained in said one frame period,

wherein said one frame period comprises 2^{m-n} subframe periods, and

wherein an image is displayed by an image gray scale of $(2^m - (2^{m-n} - 1))$ patterns.

7.(Amended) A display device comprising:

an active matrix circuit comprising a plurality of pixel TFTs disposed in matrix over a substrate;

a source driver and a gate driver which drive said active matrix circuit over said substrate; and

a circuit which converts m bit digital video data inputted from an external into n bit digital video data and provides said n bit digital video data to said source driver, wherein said m and said n are integers equal to or larger than 2 and satisfy $m > n$, wherein said circuit is formed over said substrate, and

wherein an image is displayed by an image gray scale of $(2^m - (2^{m-n} - 1))$ patterns.

8.(Amended) A display device comprising:

an active matrix circuit comprising a plurality of pixel TFTs disposed in matrix over a substrate;

a source driver and a gate driver which drive said active matrix circuit over said substrate;

and

a circuit which converts m bit digital video data inputted from an external into n bit digital video data and provides said n bit digital video data to said source driver, wherein said m and said n are integers equal to or larger than 2 and satisfy $m > n$, wherein said circuit is formed over said substrate,

wherein one frame image comprises 2^{m-n} subframes, and

wherein an image is displayed by an image gray scale of $(2^m - (2^{m-n} - 1))$ patterns.

Cancel Claim 75.

Cancel Claim 83 (second occurrence).